

004027-840700

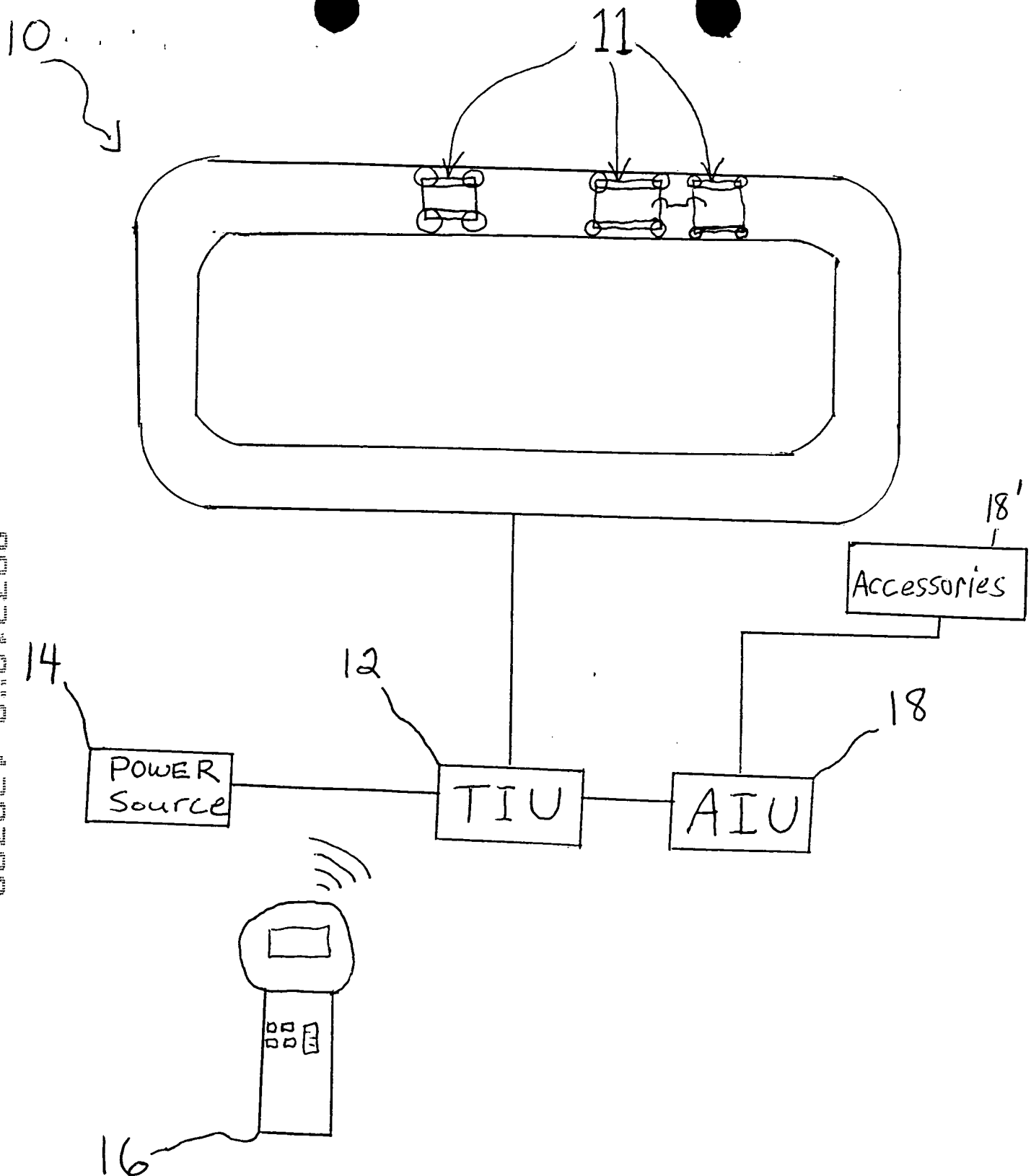


Figure 1

09731048-120700

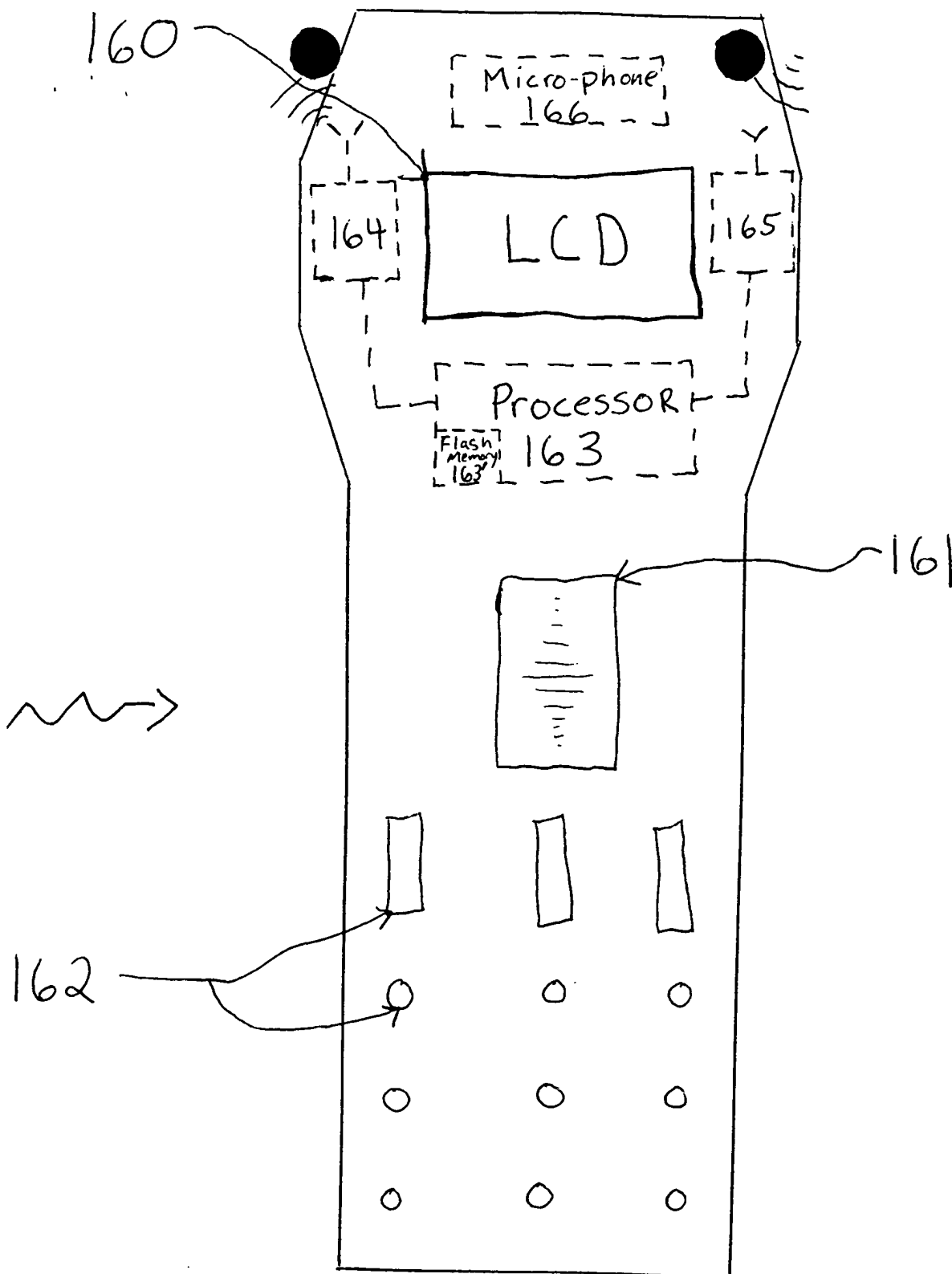


Figure 2

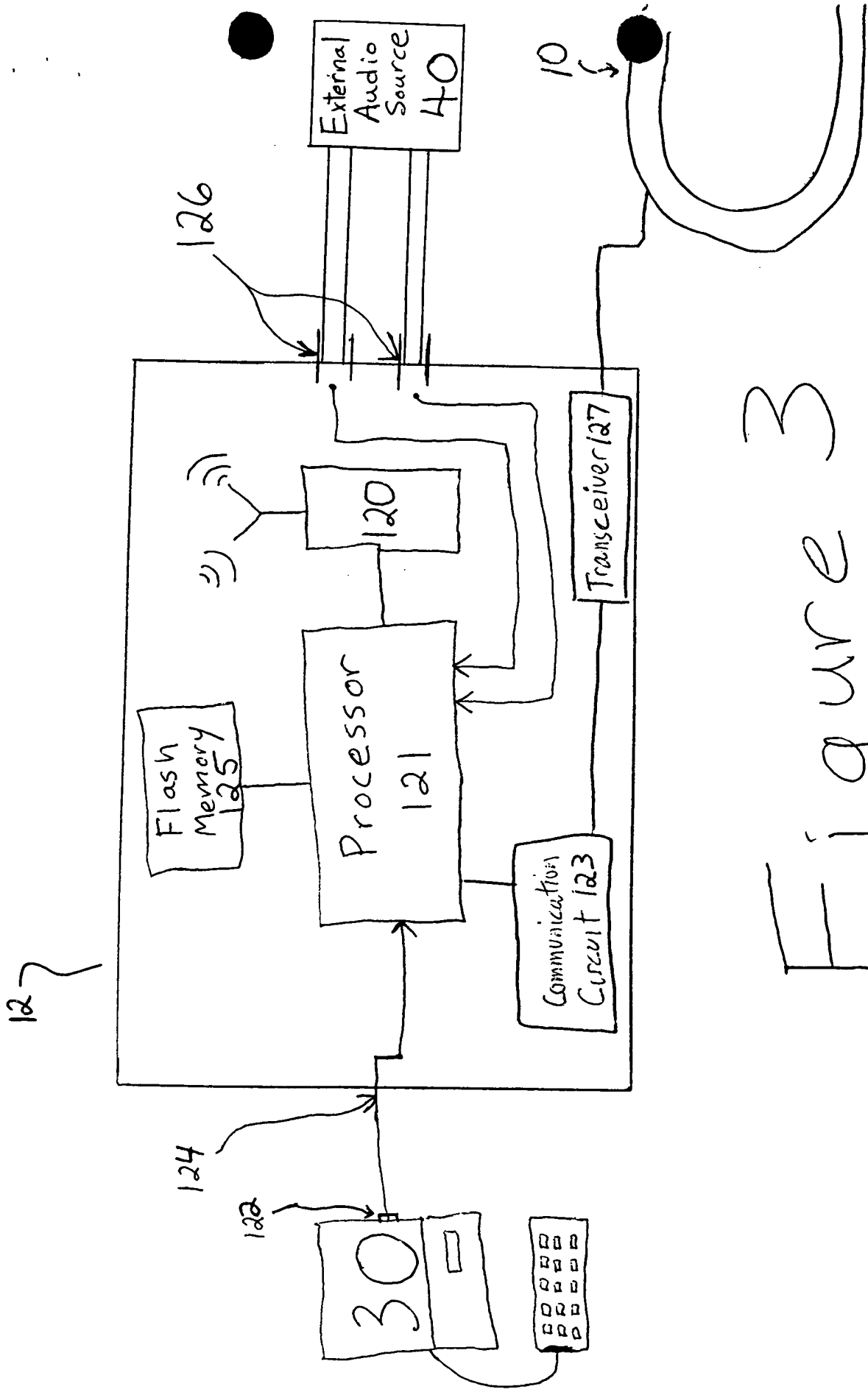
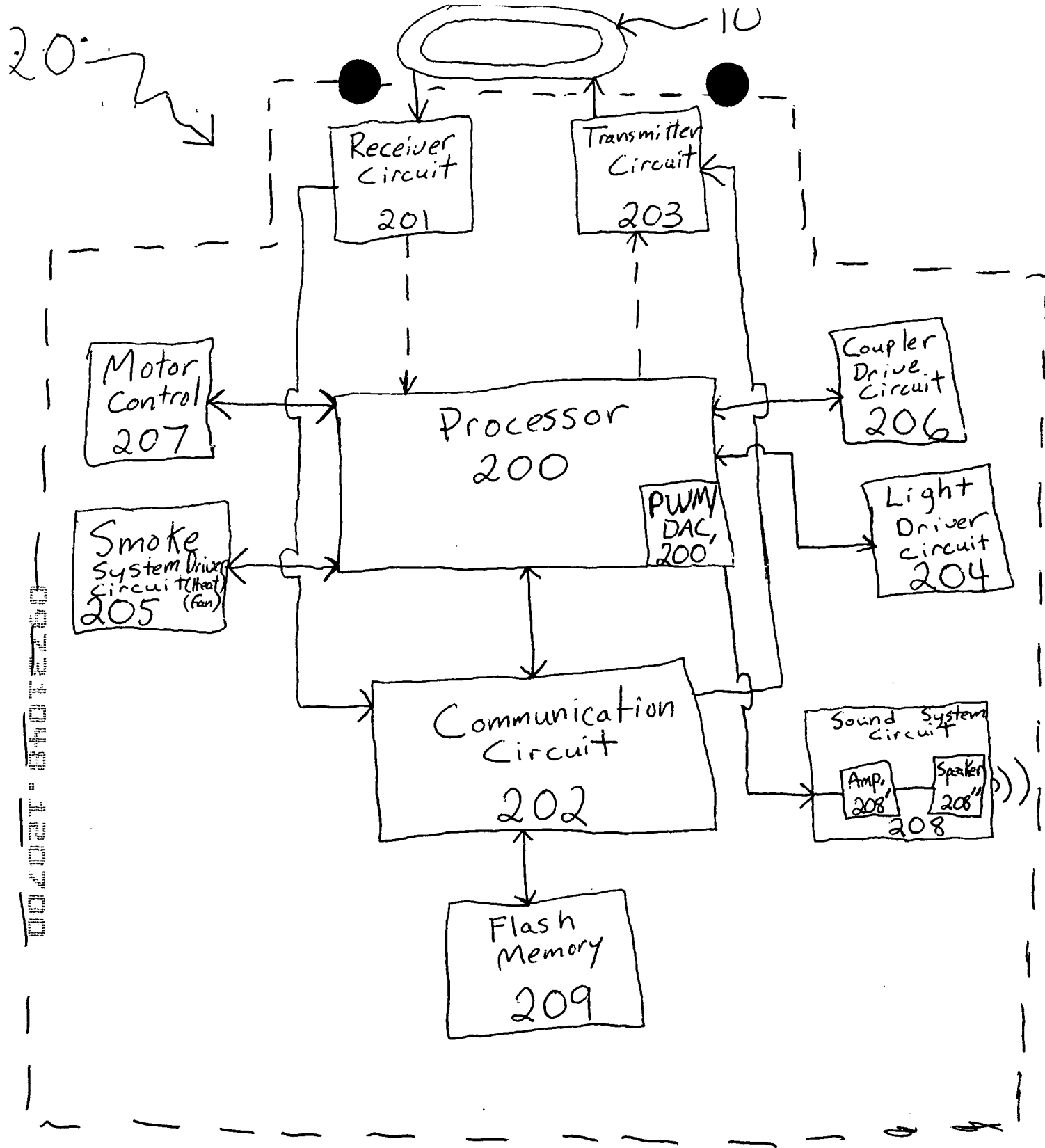


Figure 3



20

00402T 8407E 260

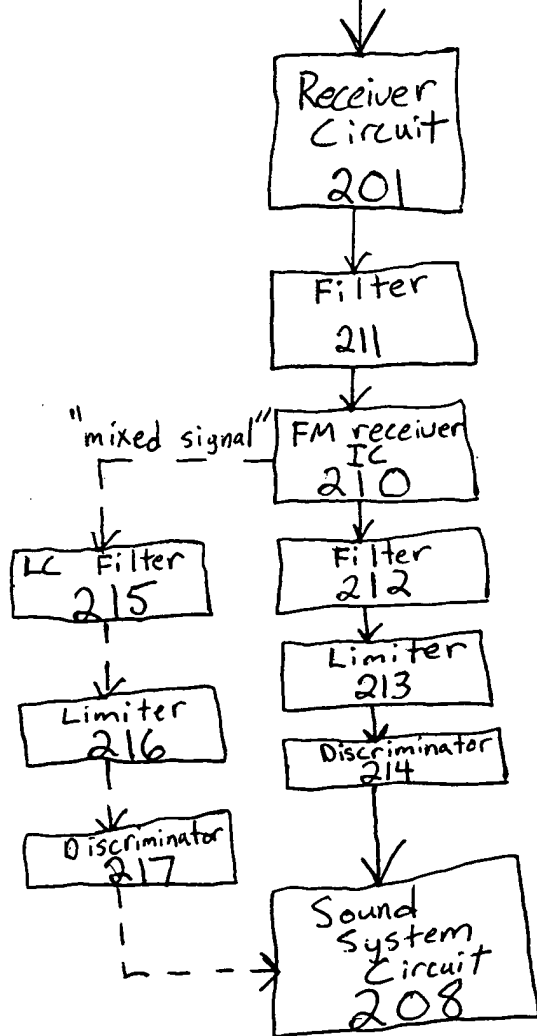
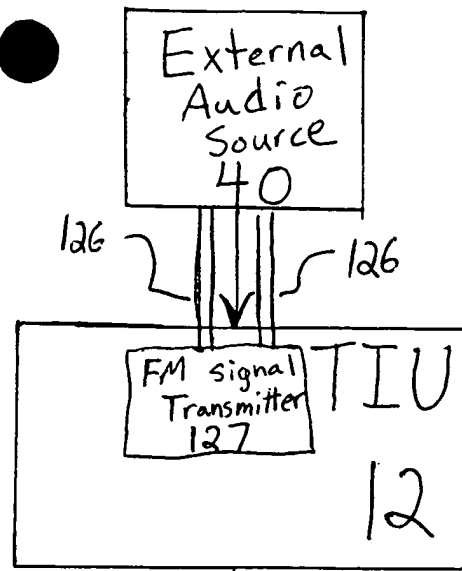


Figure 4A

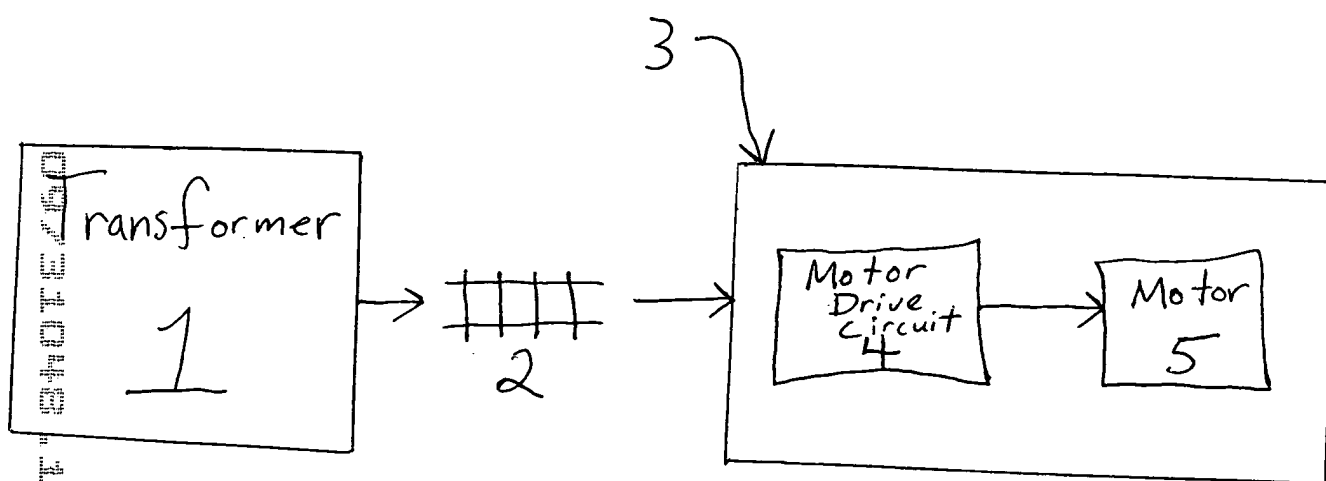


Figure 5
(Prior Art)

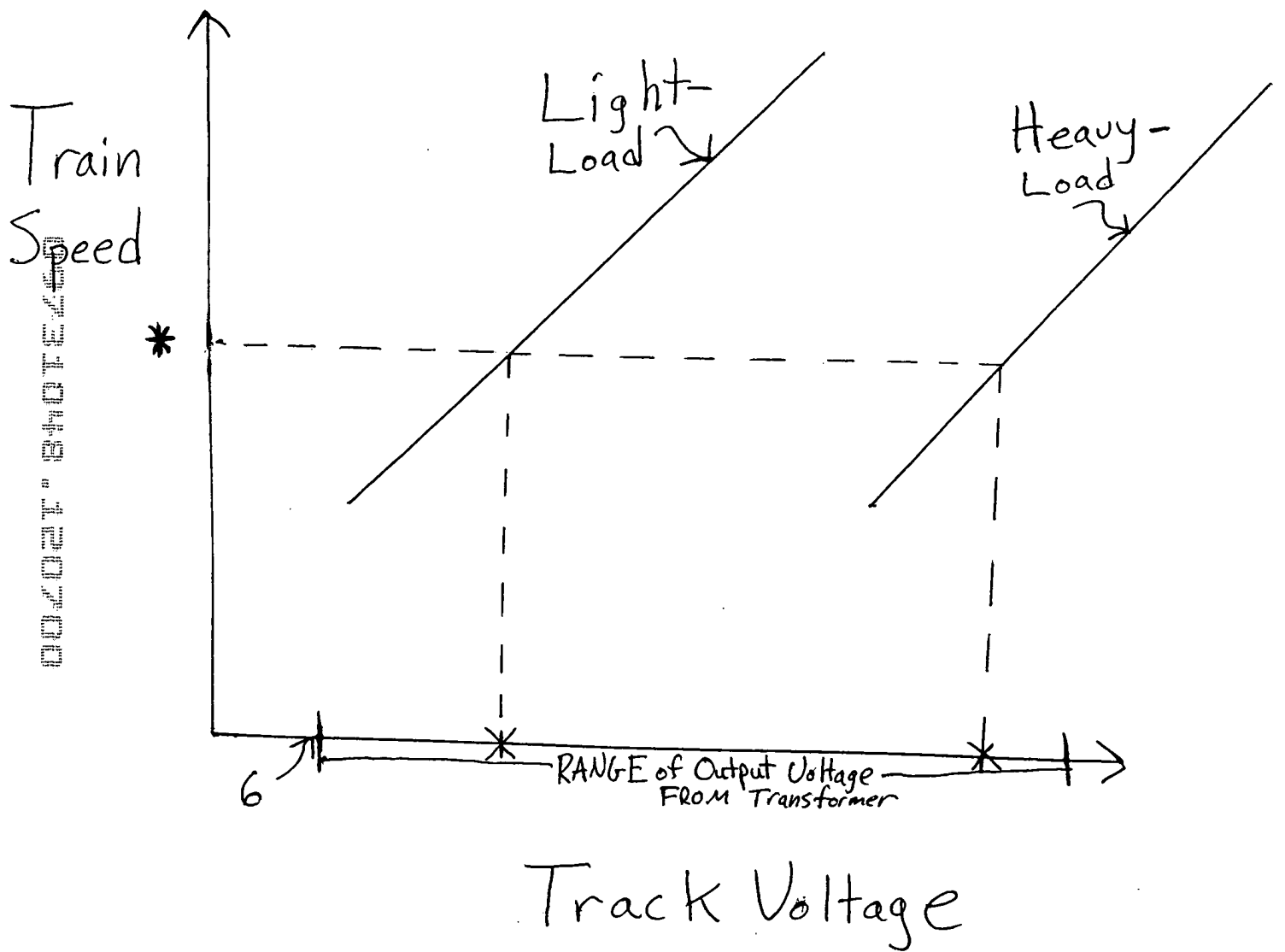


Figure 6

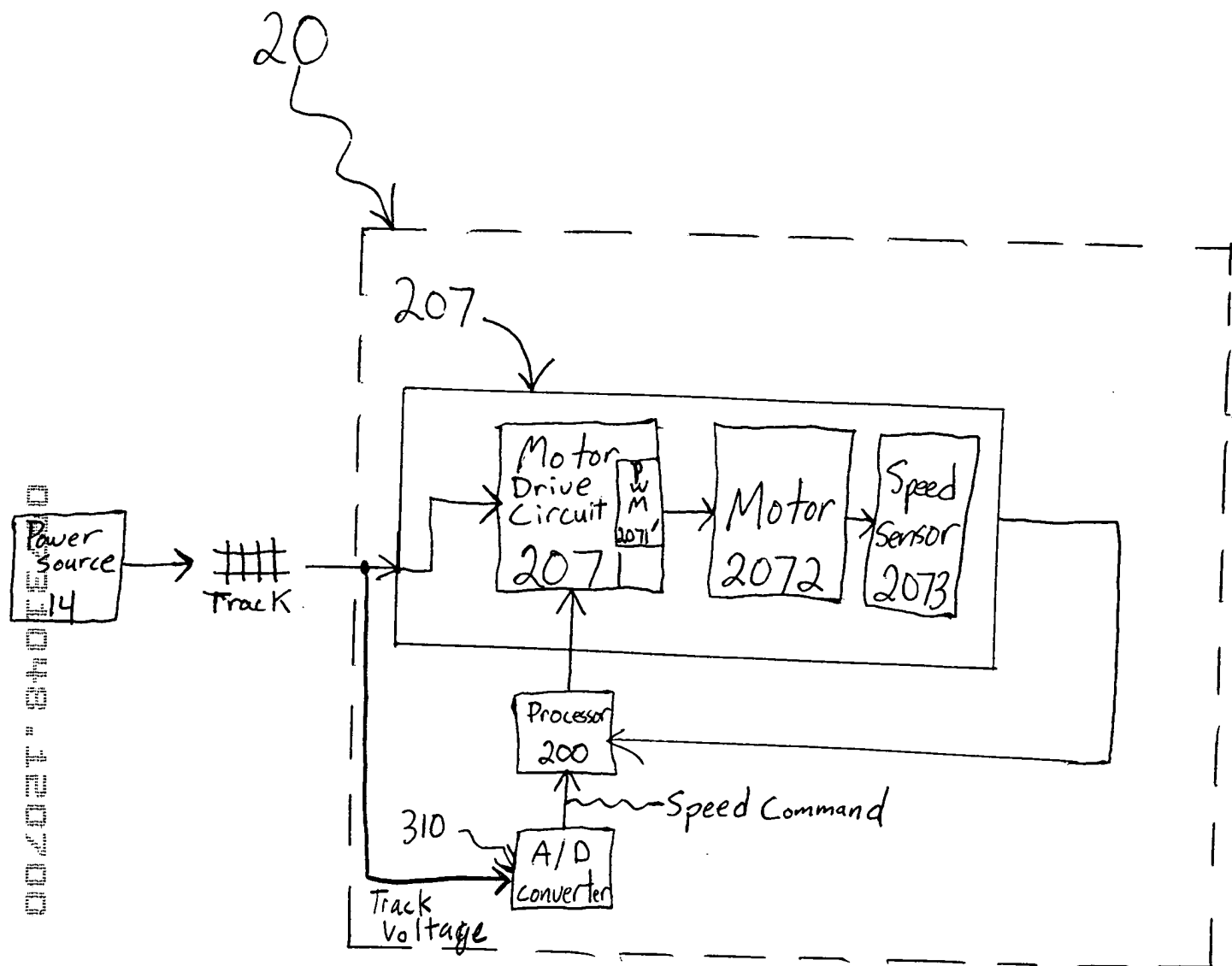


Figure 7

V^+

Rectified Track
Voltage

2071'

direction
relay

M

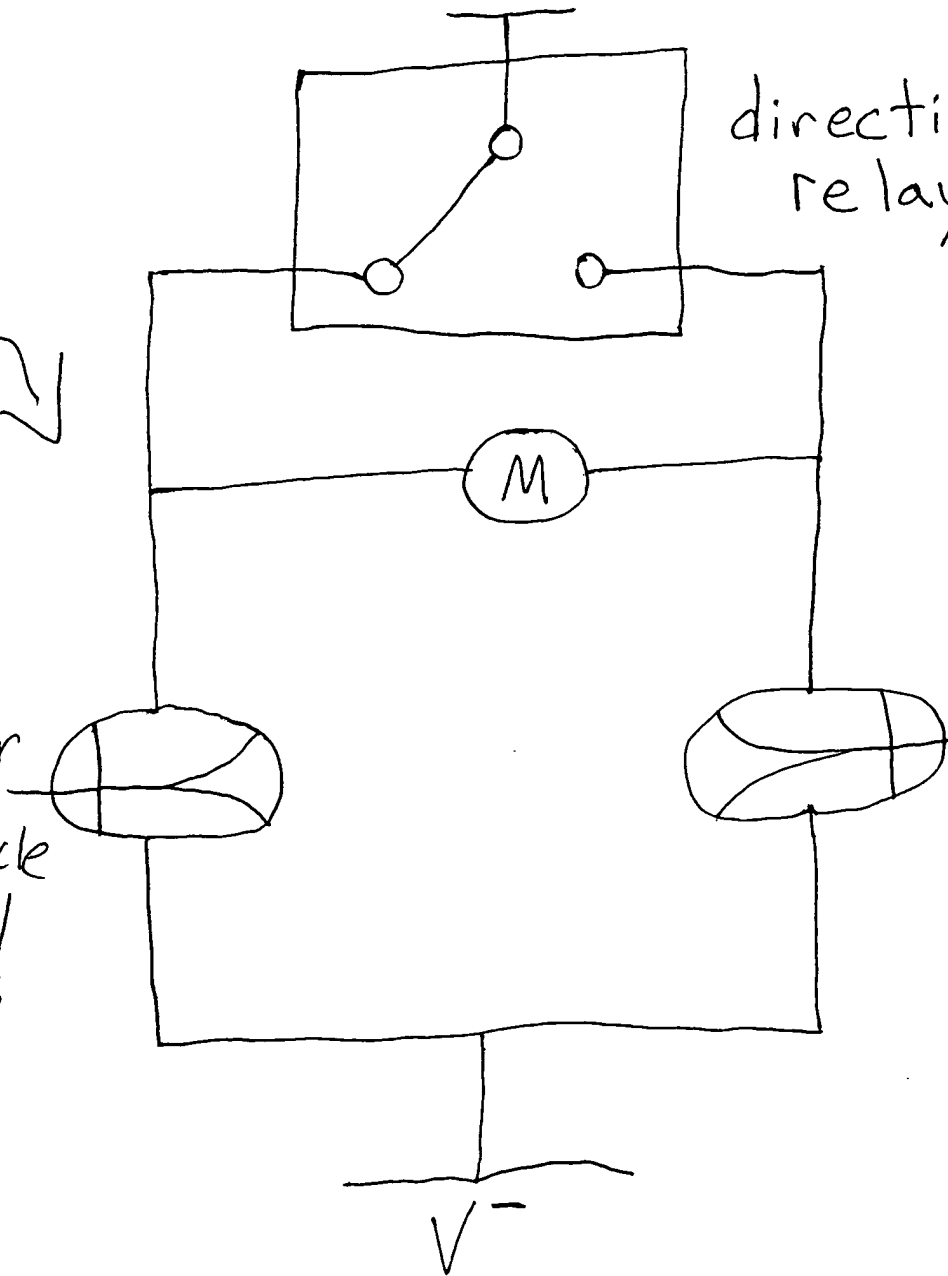
Processor
duty-cycle
Control
Pulses

Processor
duty-cycle
control
pulses

V^-

Figure 8

09731048-120700



Train
Speed

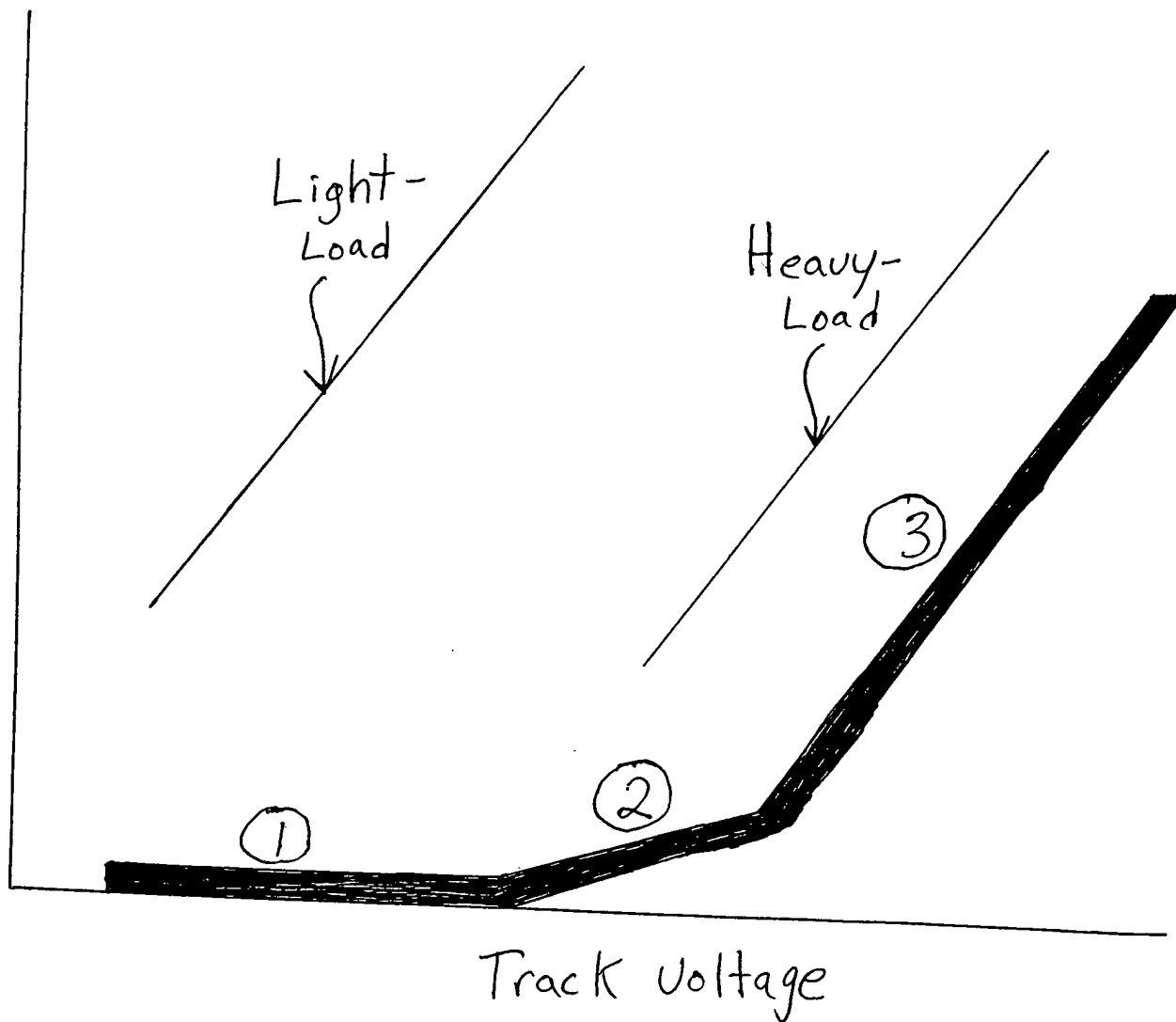


Figure 9

Figure 10a
(Prior Art)

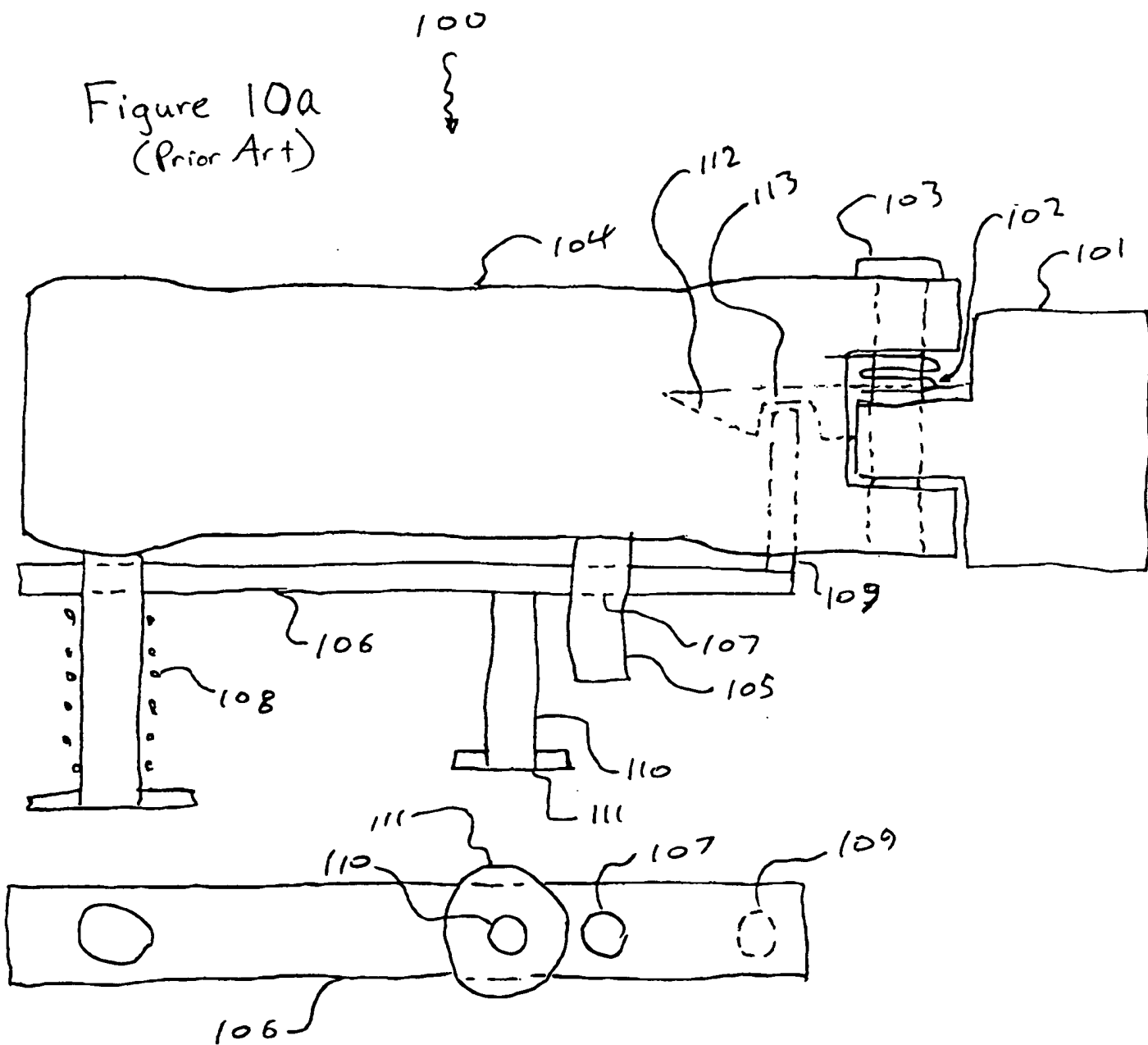


Figure 10b
(Prior Art)

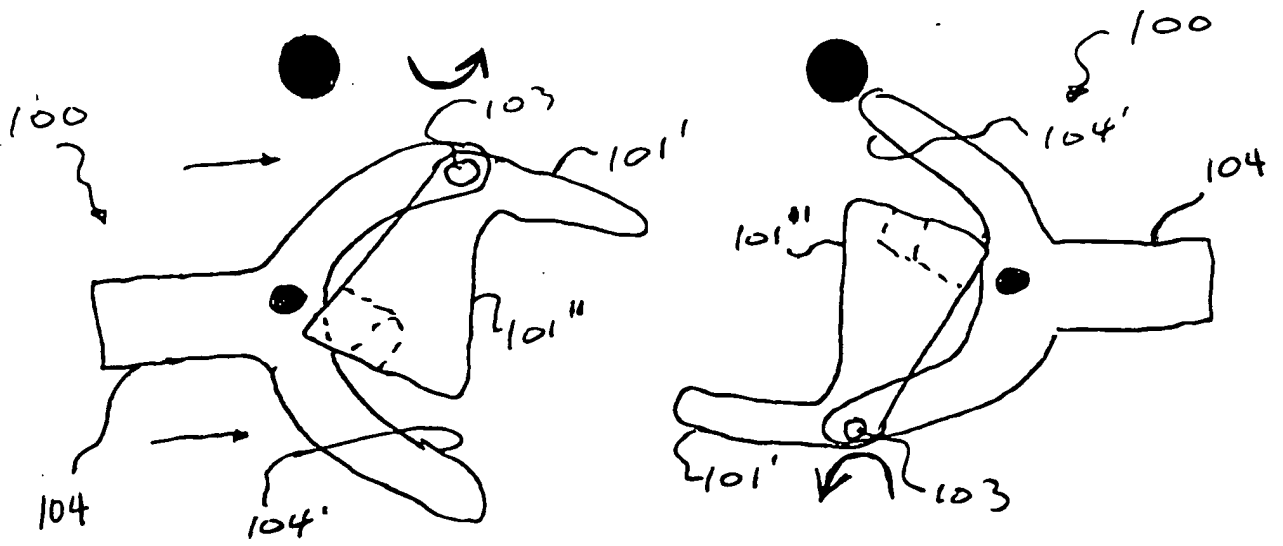


Figure 11a
(Prior Art)

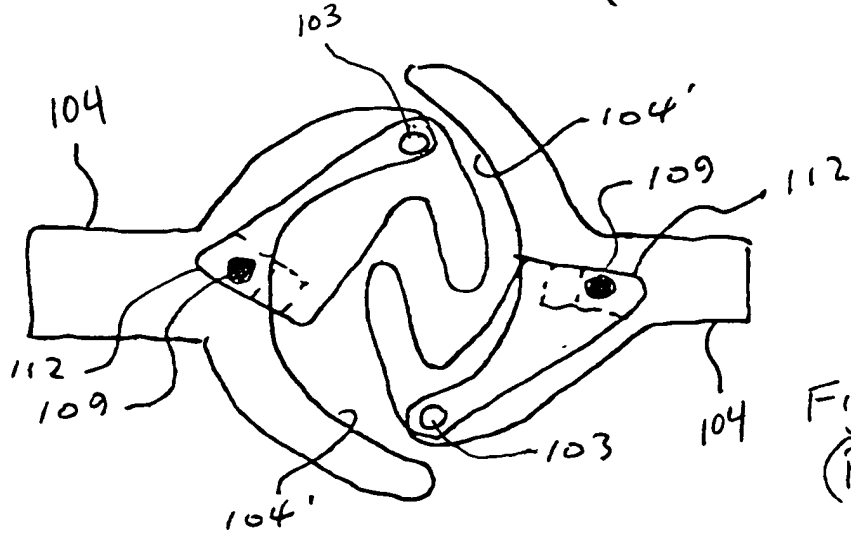


Figure 11b
(Prior Art)

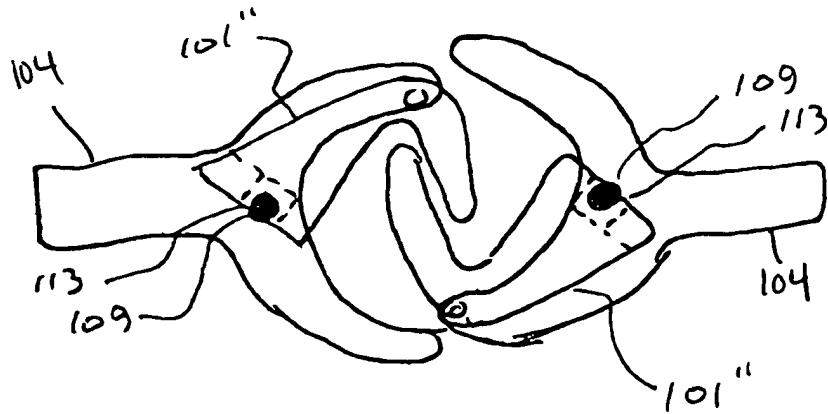


Figure 11c
(Prior Art)

150

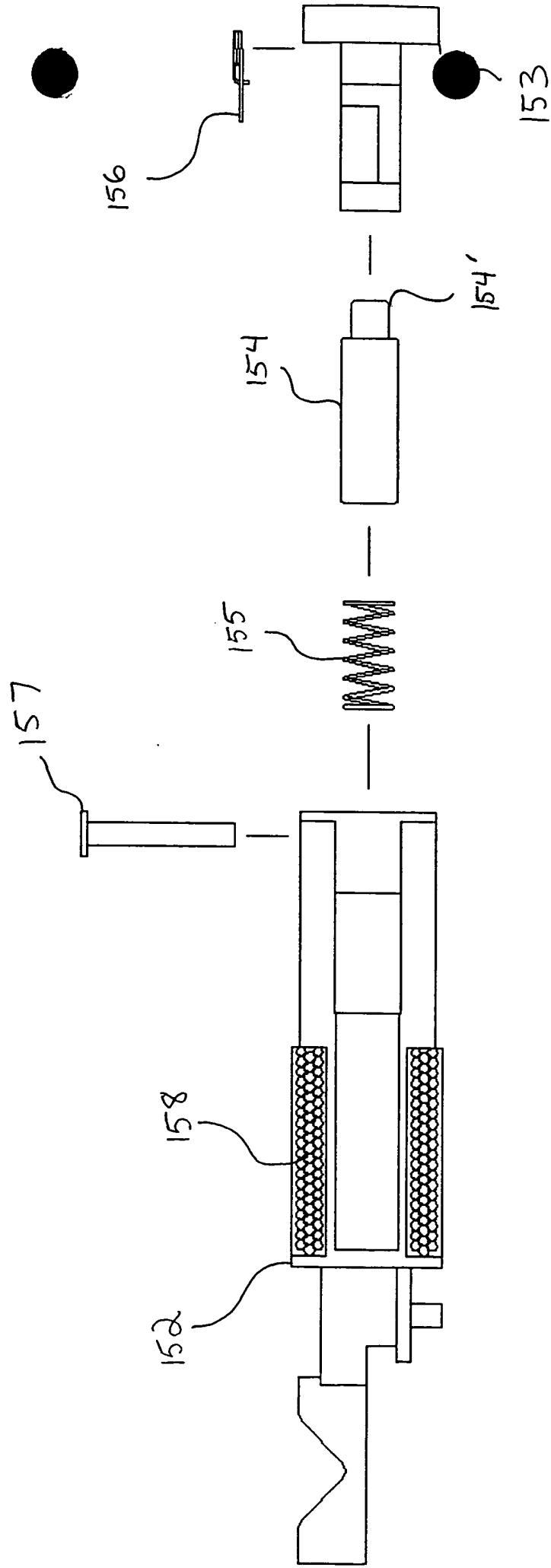
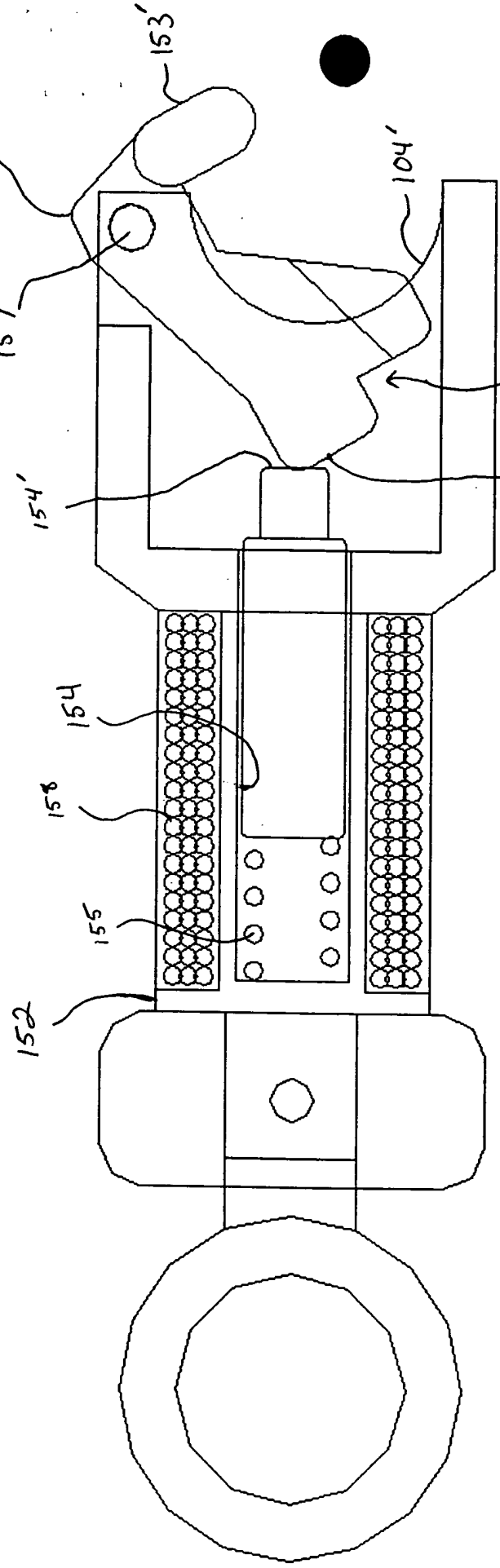


Figure 12a
(Prior Art)

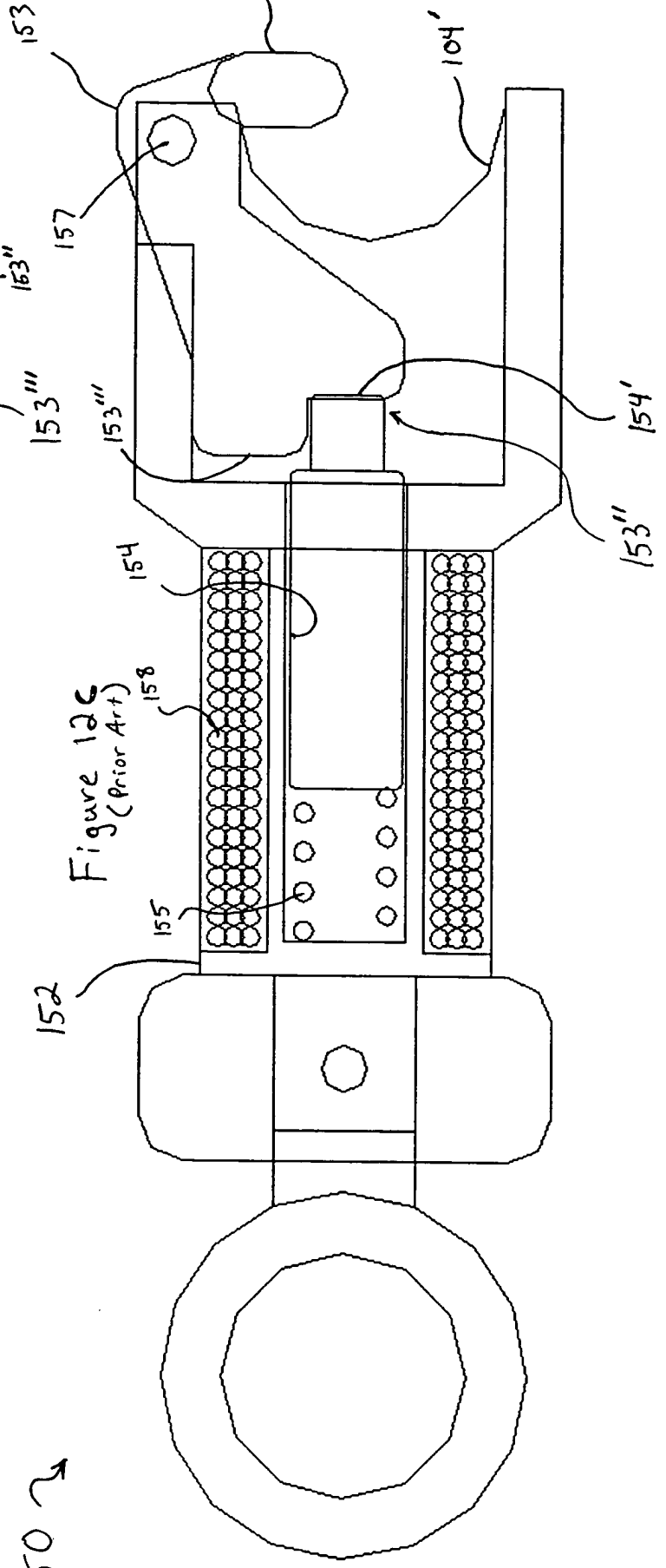
150

FIGURE 12b
(Prior Art)



150

FIGURE 12c
(Prior Art)



206

004027 8407E60

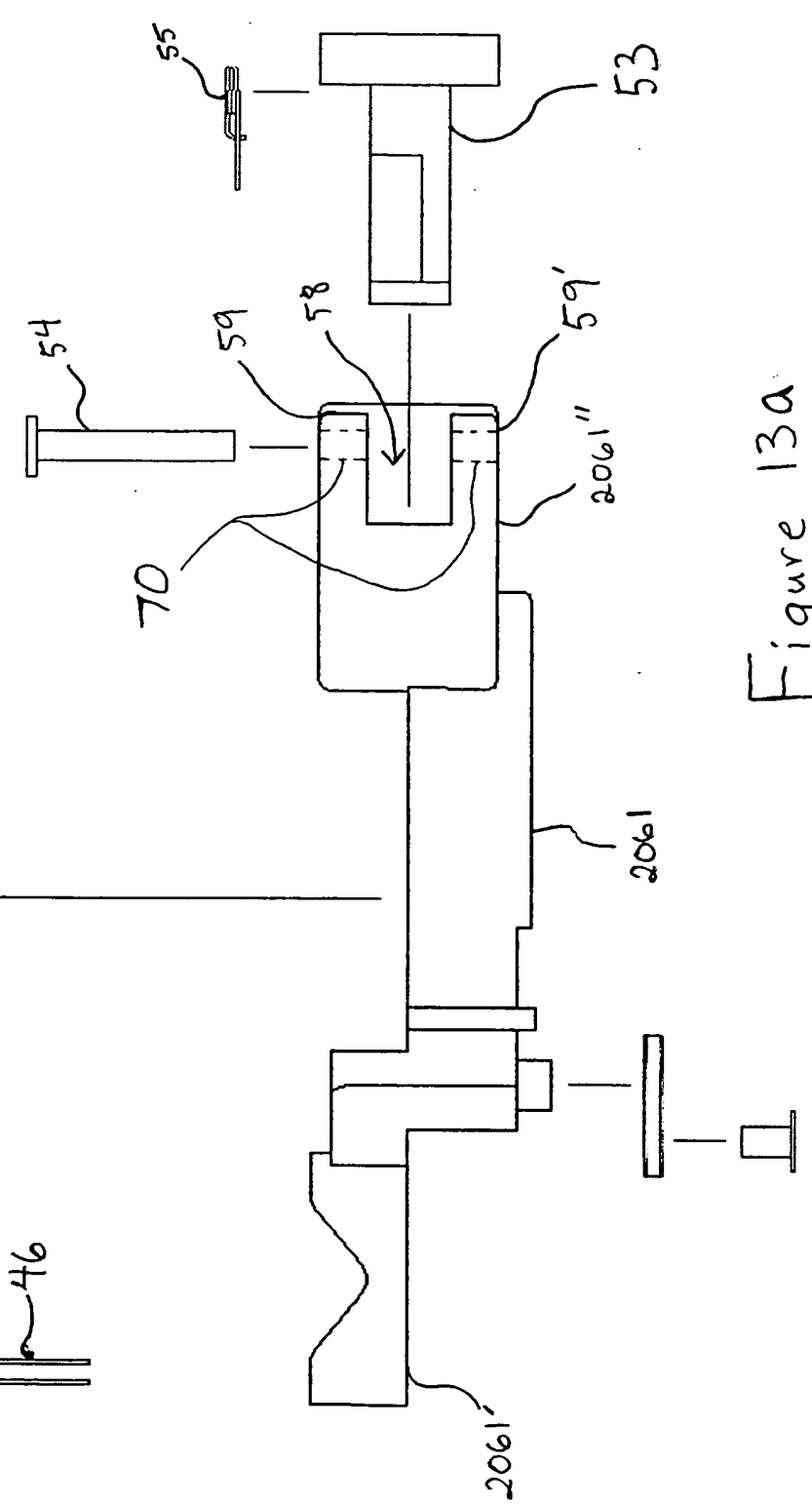
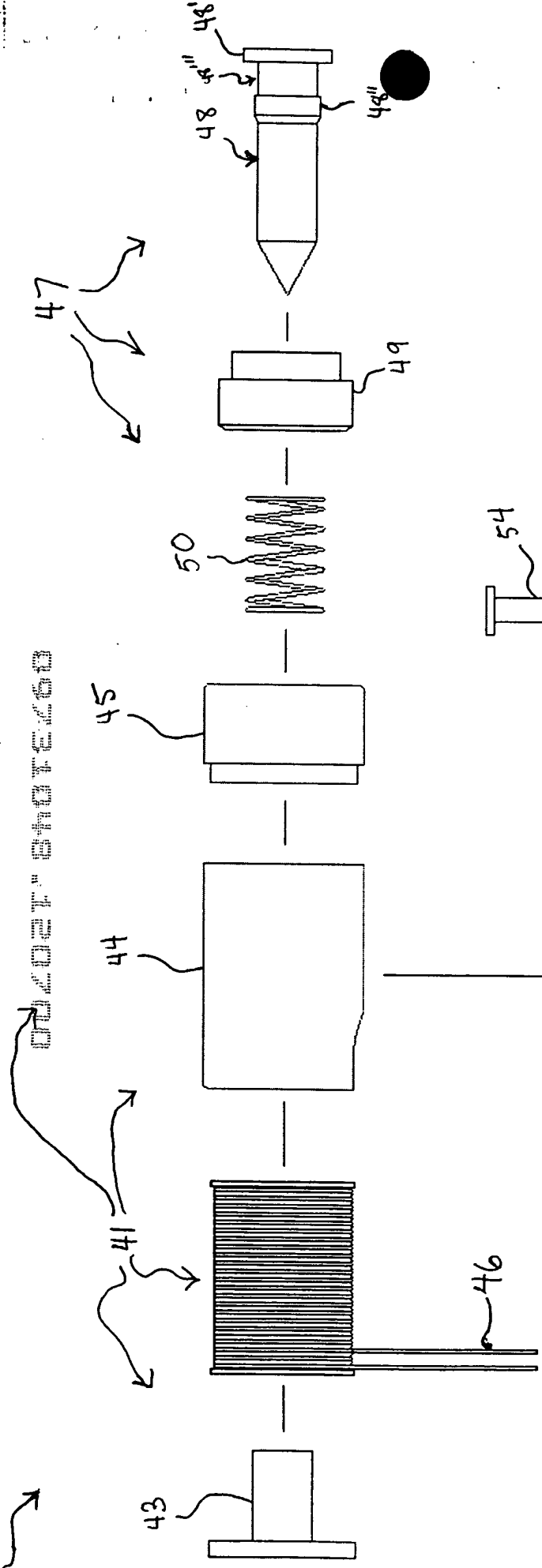


Figure 13a

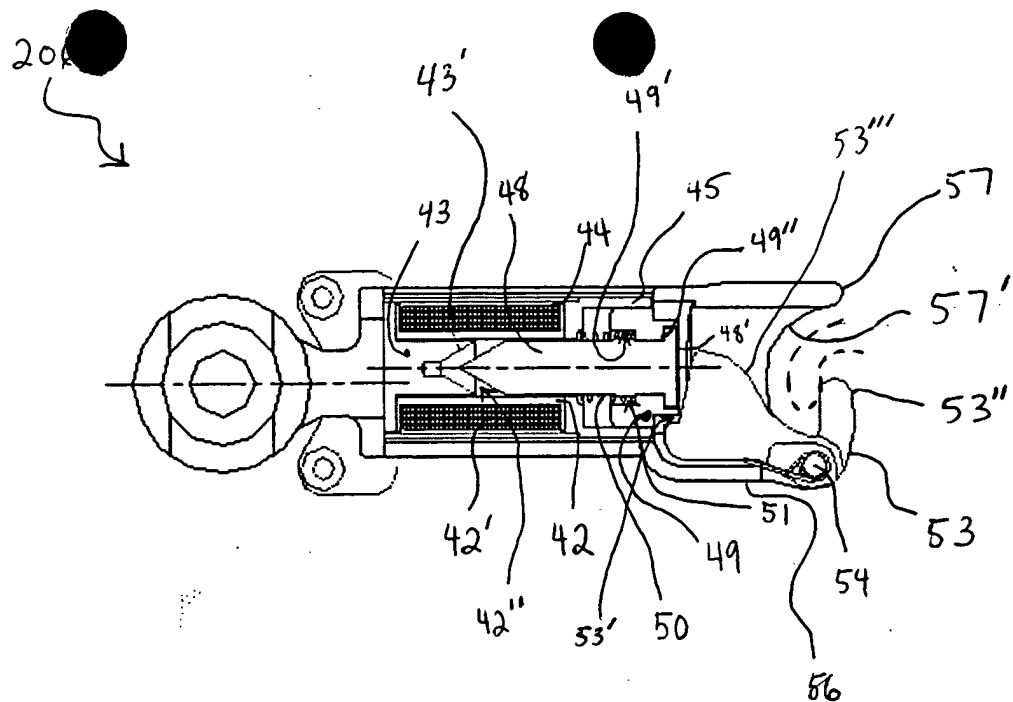


Figure 13b

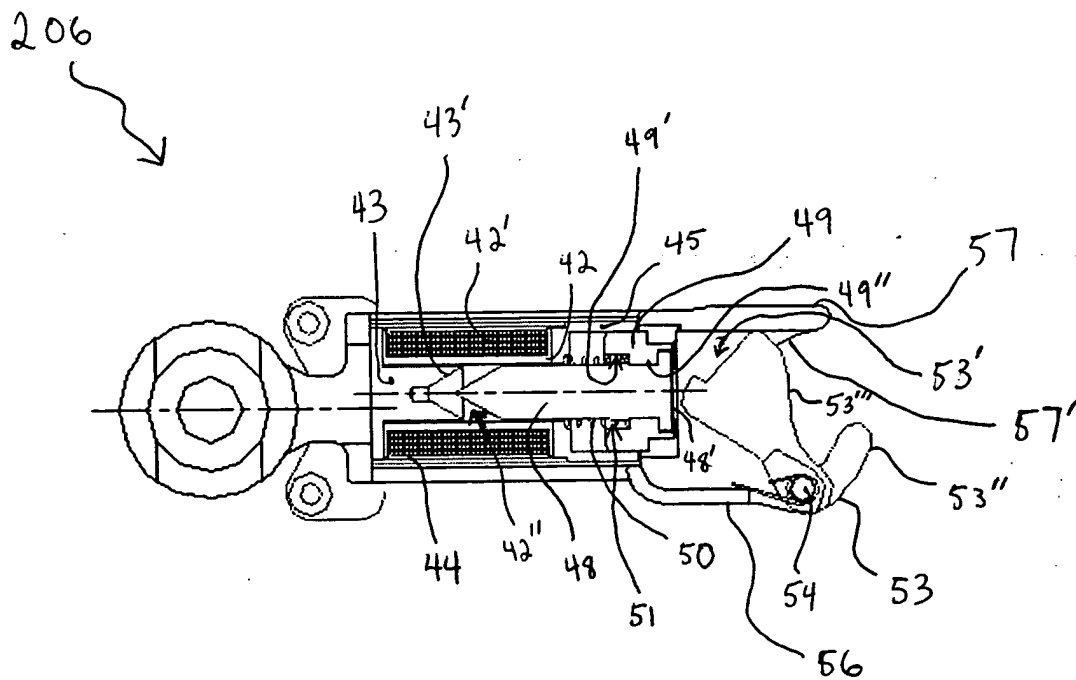


Figure 13c

Figure 13d

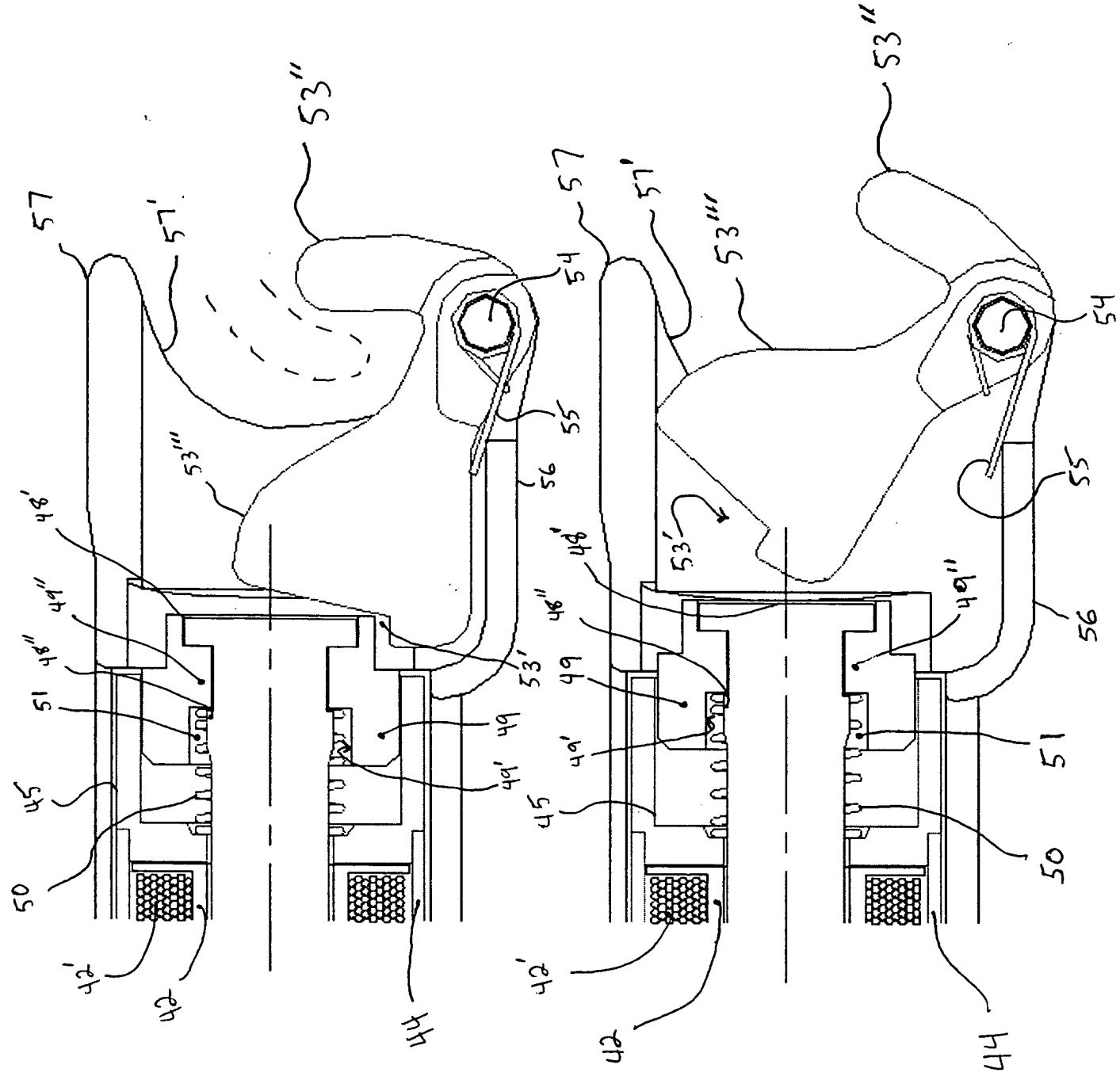


Figure 13e

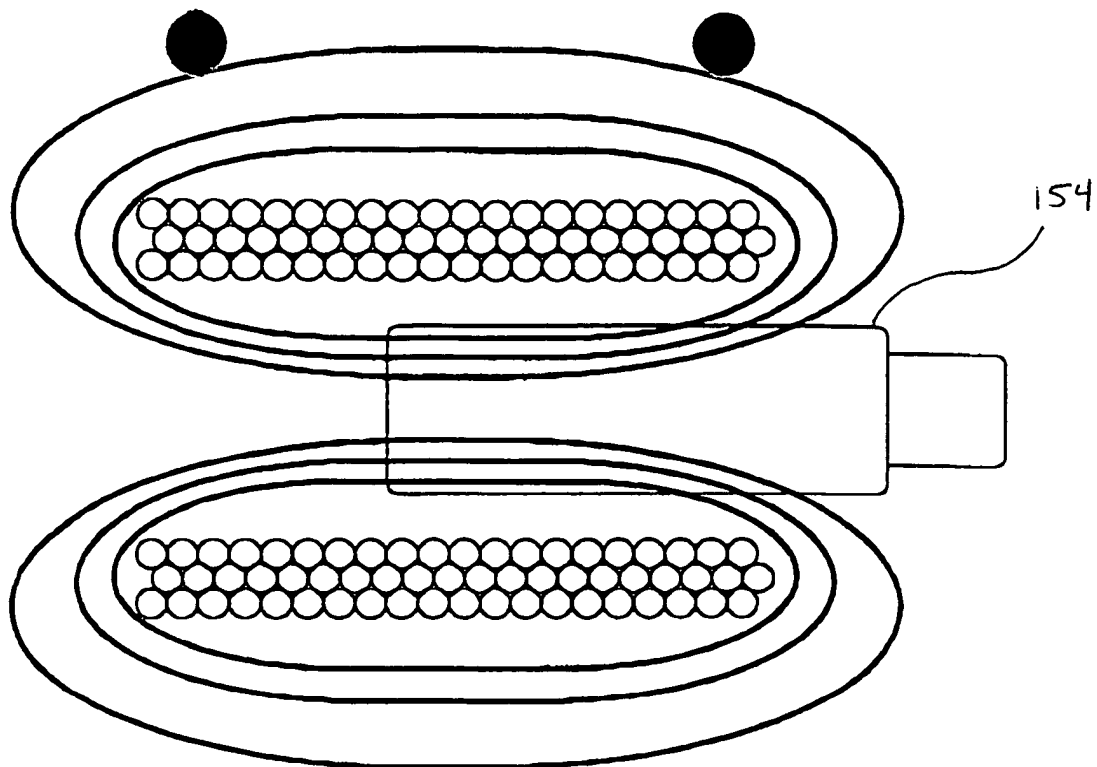
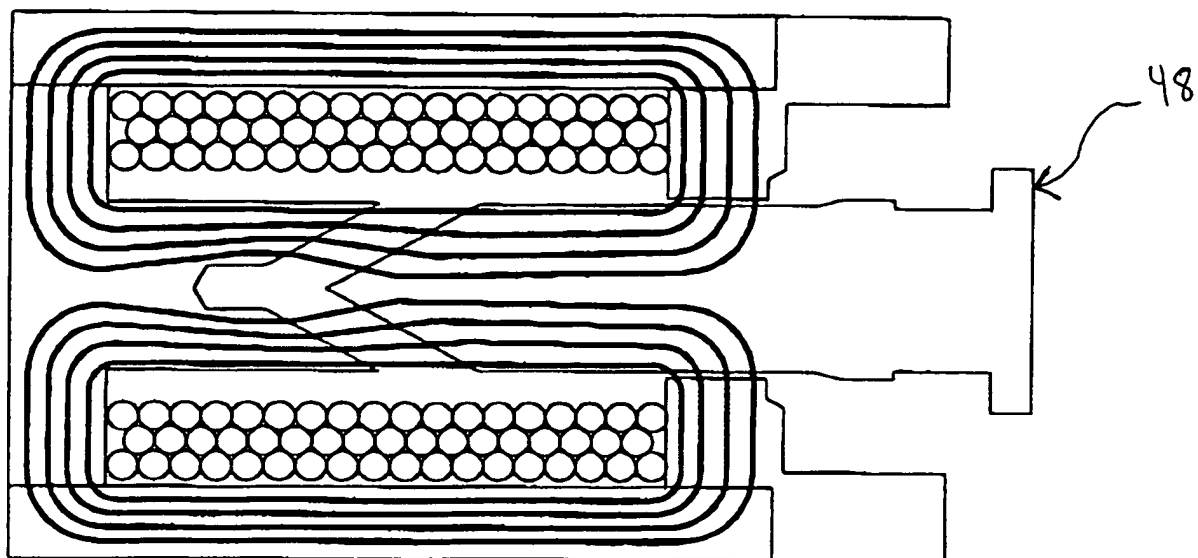


Figure 13f
(Prior Art)

Figure 13g



144

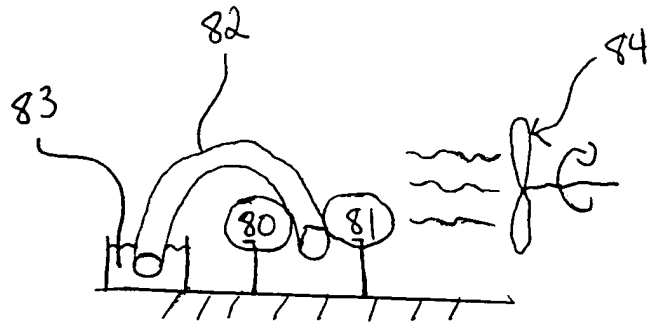


Figure 14a

144

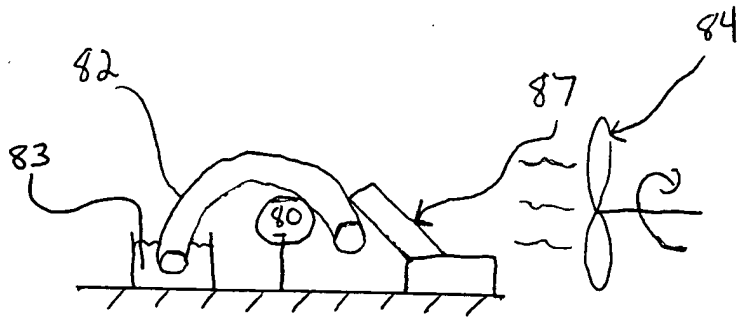


Figure 14b

09731048-120700

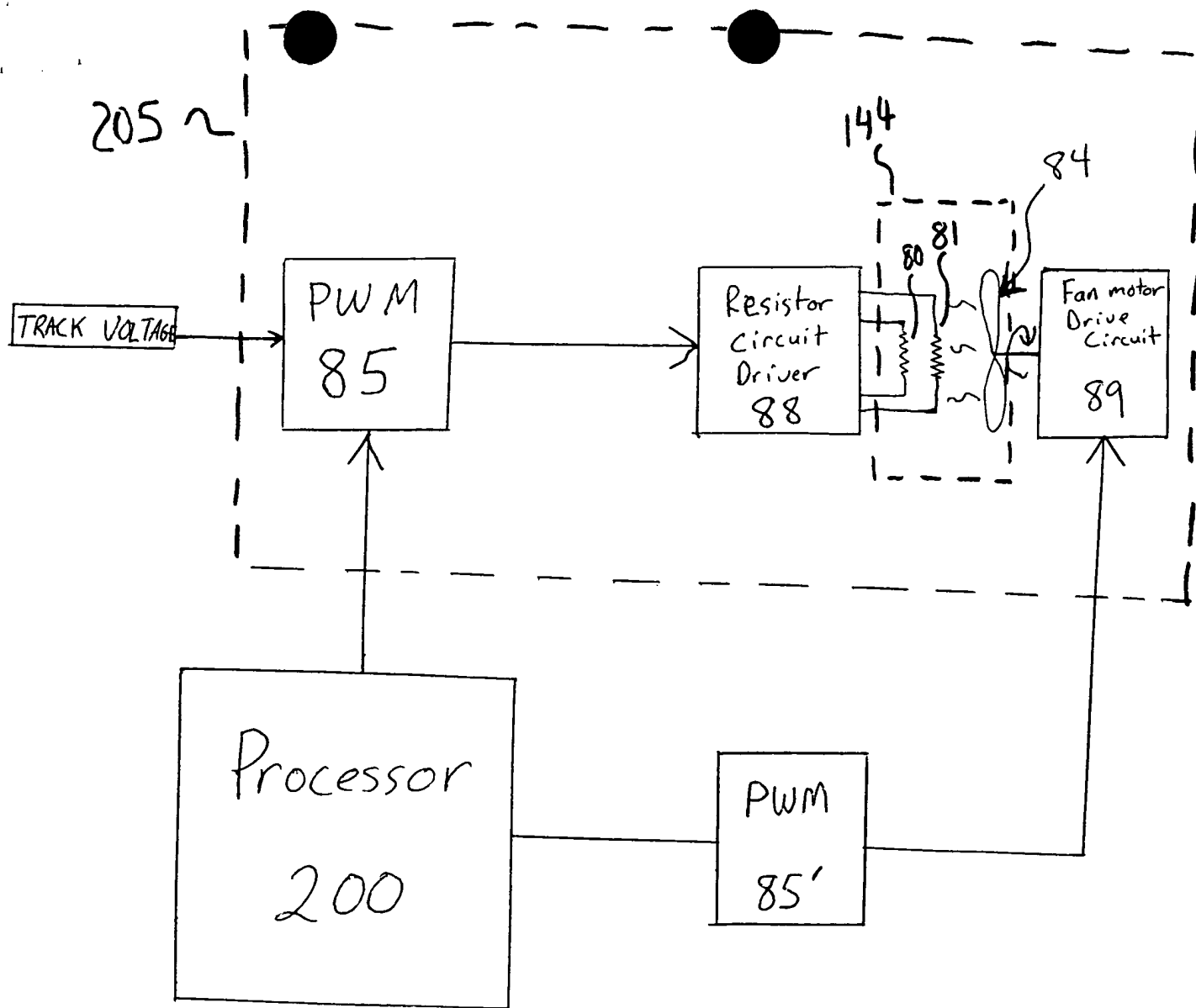


Figure 14c

P-N codes →

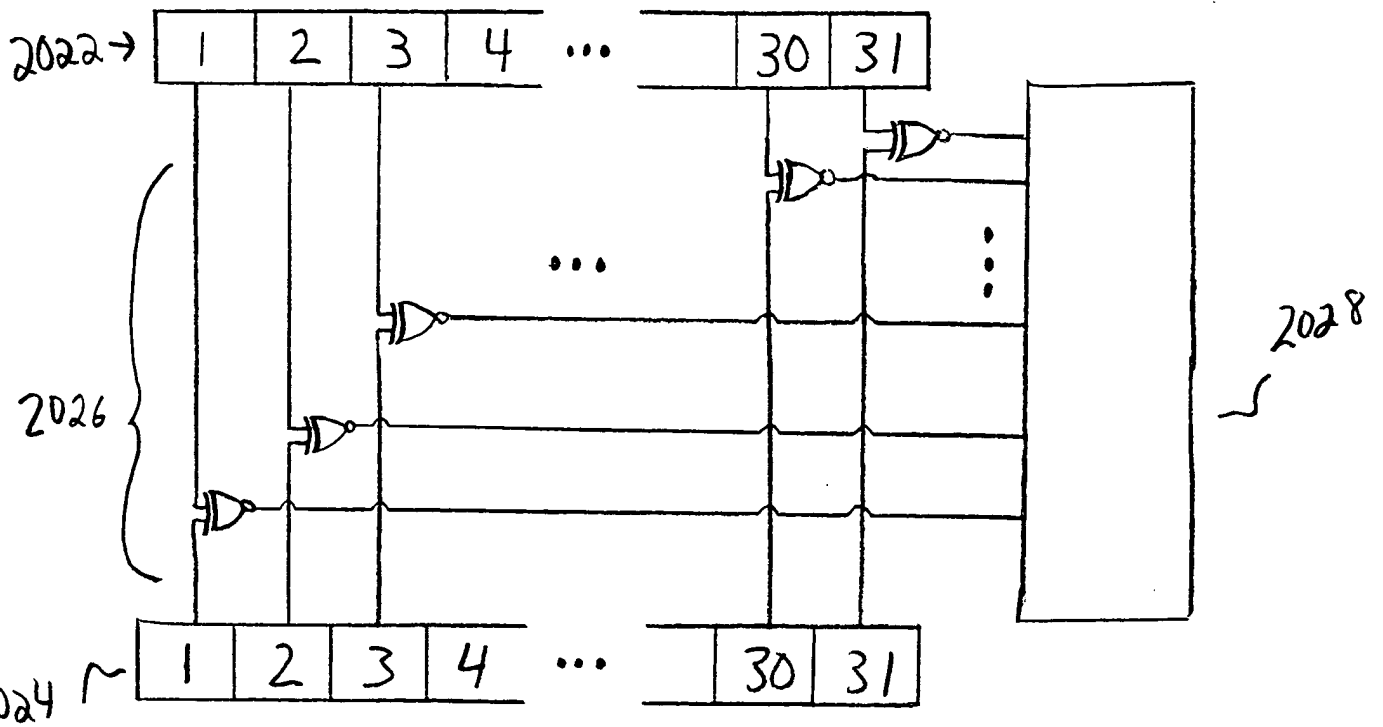


Fig. 15a [IDEAL CASE]

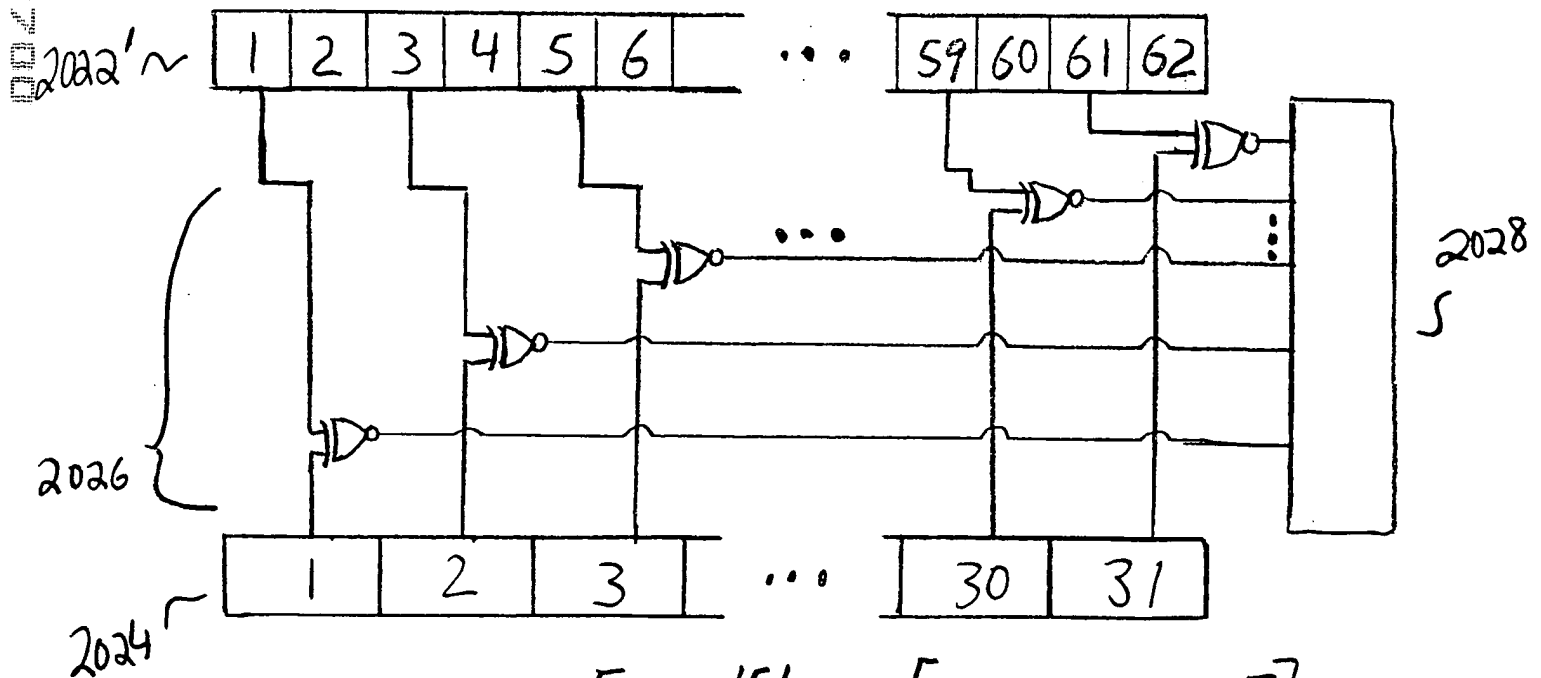
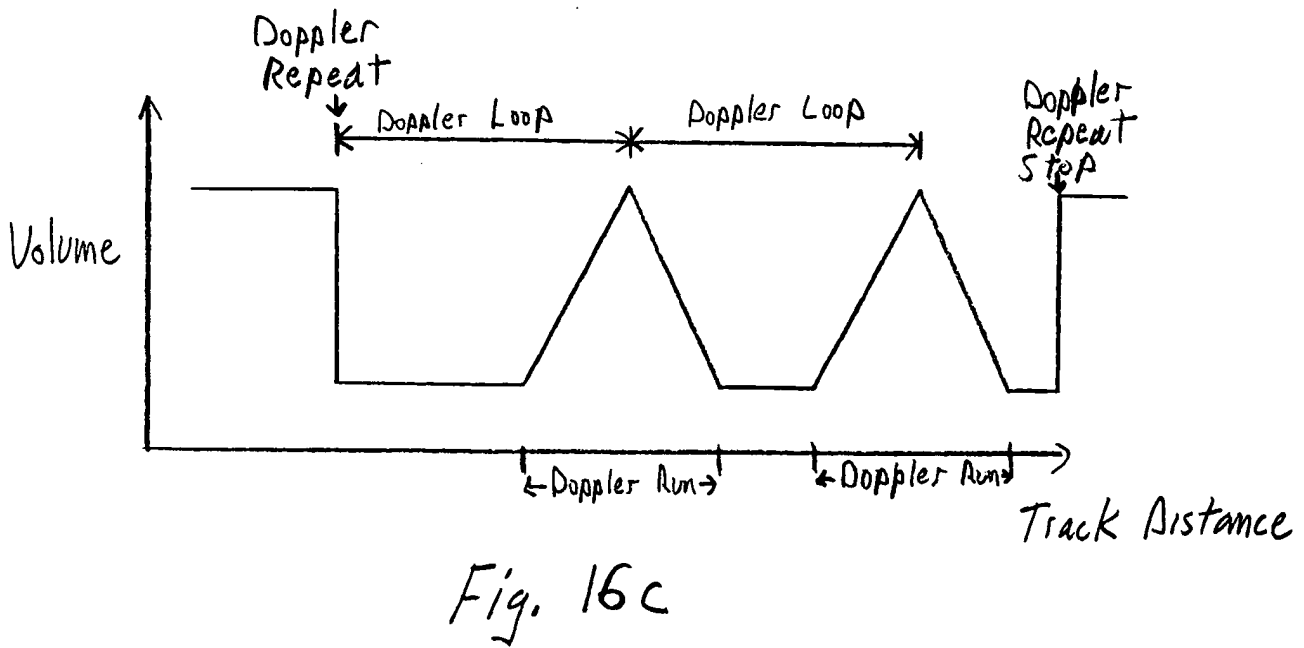
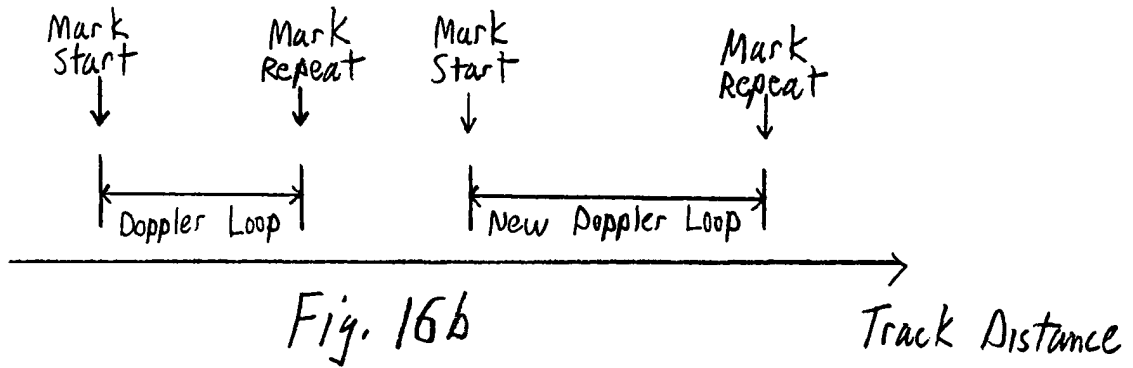
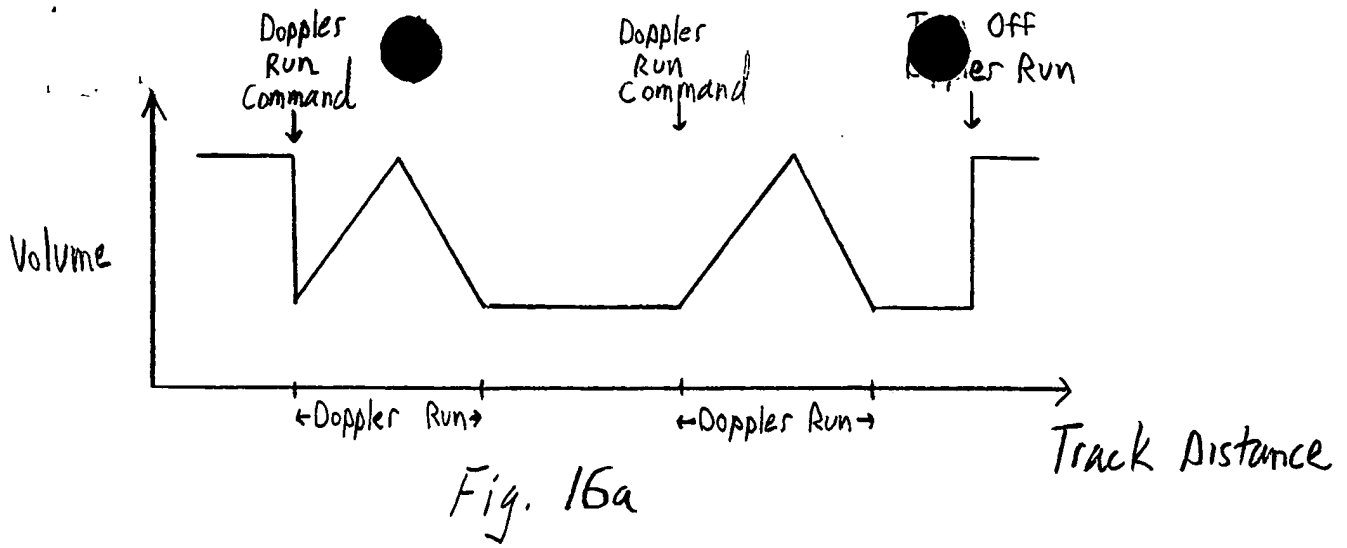


Fig. 15b [NOISY ENVIRONMENT]



09731048-120700

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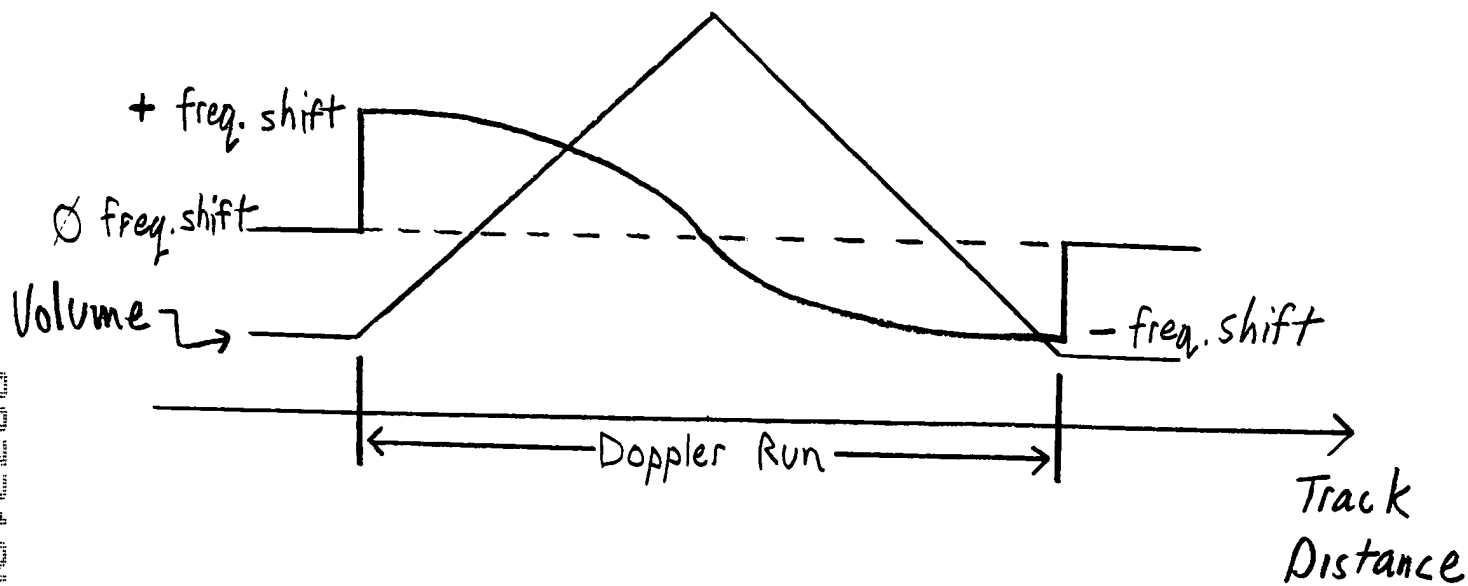


Fig. 16d

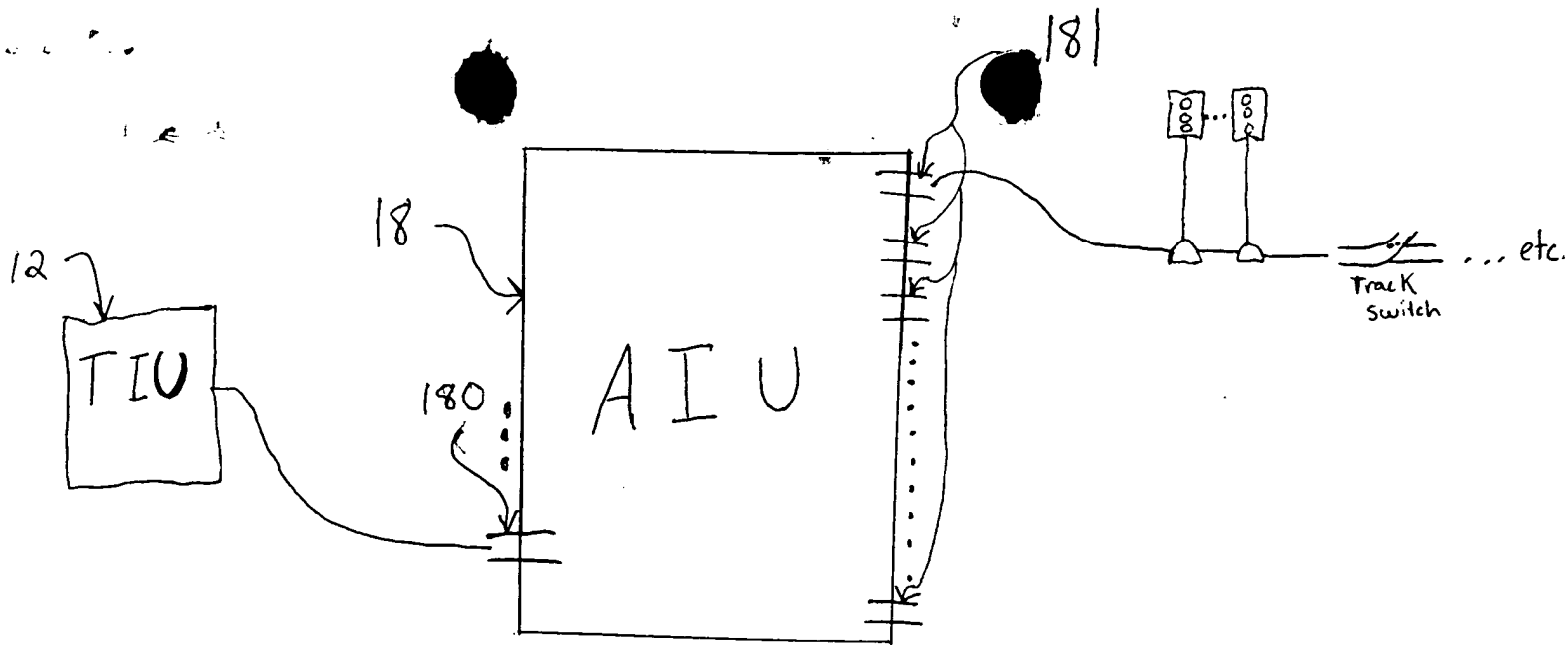


Figure 17a

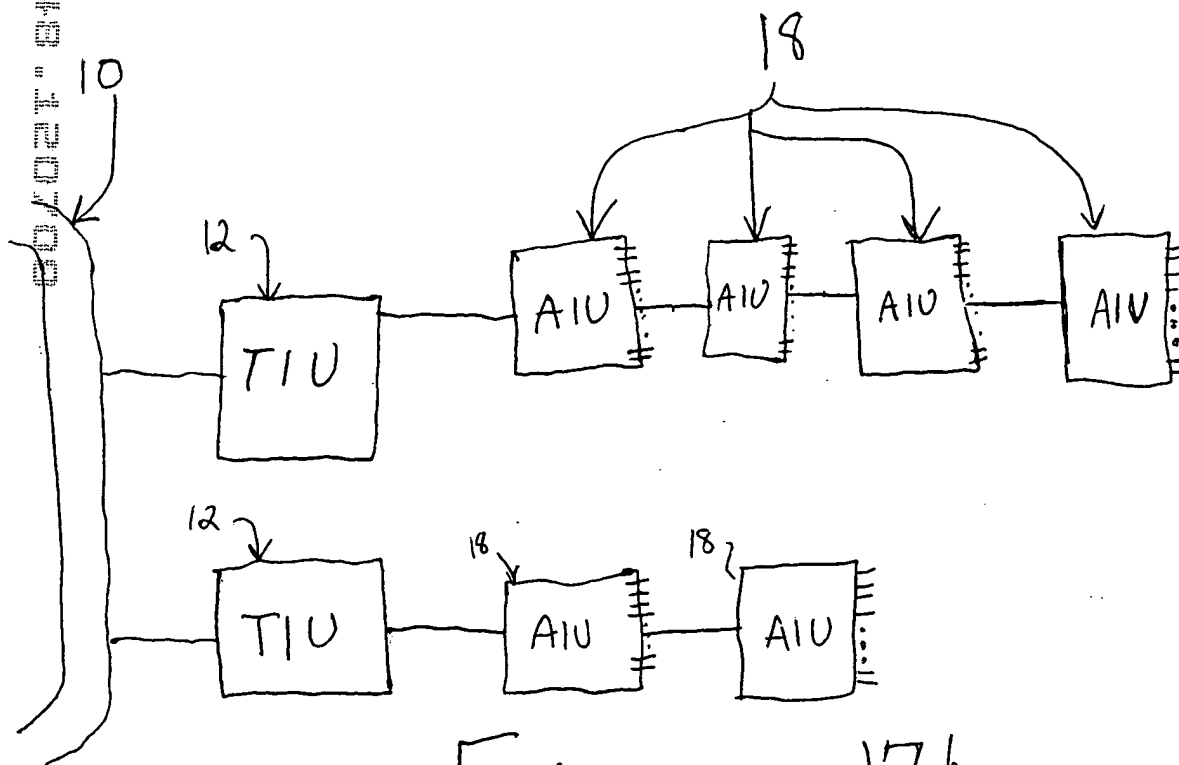


Figure 17b